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,د				BRS form A IS&R form Marge Mar	<u>ы</u> німг			
_	Туре	L#	Hits	Search Text	DBs	Time Stamp	근	弄
	BRS	L2	8832	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and controlS4	1		+	+
		<u> </u>		and (load or lamp)	DERWENT; IBM TDB	09:33	4	1
	BRS	L3	5432	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and (controlS4 same (load or lamp))		2005/07/19 09:12	1	
	BRS	L4	4656	3 and ((power near2 switchS3) or drivS3 or (switchS3 near2 device))	US-PGPUB; USPAT; USOCR; EPO; JPO;		+	+
						09:33	4	1
	BRS	L5	674	4 and (set near2 point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:34		
	BRS	L6	433	4 and (set adj point)	US-PGPUB; USPAT; USOCR; EPO; JPO;		\forall	十
						09:15	_	\downarrow
	BRS	L7	135	6 and (minimS2 near3 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:35		
	BRS	L8	125	7 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO:		1	+
						10:41		4
	BRS	L9	4629	(memory or (storage adj device)) and (oscillat\$3 same digital\$2) and controller and (load or lamp)		2005/07/19 09:33		
	BRS	L10	4083		US-PGPUB; USPAT; USOCR; EPO; JPO;		1	+
			<u> </u>		DERWENT; IBM TDB	09:33		_
0	BRS	L11	1000	10 and ((set near2 point) or (referenceS near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:09		
1	BRS	L12	312	11 and (minimS2 near3 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO:		+	+
		<u> </u>	<u> </u>			10:09		_
2	BRS	L14	9	13 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:39		
3	BRS	L13	13	((digitalS2 near3 oscillator) same ((set adj point) or (reference adj level))) and	US-PGPUB; USPAT; USOCR; EPO; JPO;		+	+
			<u> </u>	(memory or (storage adj device))	DERWENT; IBM TDB	11:46		
4	BRS	L15	331	(digital\$2 near3 oscillator) and ((set adj point) or (reference adj level)) and (memory or (storage adj device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:39		
5	BRS	L17	200	15 and load	US-PGPUB; USPAT; USOCR; EPO; JPO;		+	\dagger
		ļ			DERWENT; IBM TDB	10:41	_	_ _
6	BRS	L19	168	17 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:41		
7	BRS	L18	171	16 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO;		+	+
					DERWENT; IBM TDB	11:28		1
8	BRS	L16	208	15 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:40		
9	BRS	L28	3405	(ballast near2 controlS4)	US-PGPUB; USPAT; USOCR; EPO; JPO;		:	+
					DERWENT; IBM TDB	11:41	Ц.	丄
0	BRS	L30	371	28 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:42		
1	BRS	L31	4	30 and (digital near2 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO;		十	\dagger
			ļ		DERWENT; IBM TDB	11:46	_	_
2	BRS	L33	1	28 and ((digital\$2 near3 oscillator) same ((set adj point) or (reference adj level))) and (memory or (storage adj device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:46		
23	BRS	L32	85	30 and oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO		+	+
					DERWENT; IBM TDB	12:27	丄	1
4	BRS	L35	208	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device)) and controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT: IBM TDB	2005/07/19 13:56	ŀ	
25	BRS	L36	105		US-PGPUB; USPAT; USOCR; EPO; JPO;		+	+
					DERWENT, IBM TDB	12:38	4	1
6	BRS	L37	153	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device)) and (lamp or load)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:28		
27	BRS	L38	1		US-PGPUB; USPAT; USOCR; EPO; JPO;		+	+
					DERWENT; IBM TDB	13:57	4	_
8	BRS	L39	2	37 and ballast and controlS4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:09		
29	BRS	L40	2	37 and ballast	US-PGPUB; USPAT; USOCR; EPO; JPO;		+	+
	55.		<u> </u>	·	DERWENT; IBM_TDB	14:09	4	1
0	BRS	L41	2	40 and ((set near2 point) or (referenceS near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/19 14:10		
31	BRS	L42	70	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device))			+	+
		<u></u>	<u> </u>			14:28	丄	L.
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